

EAST - [default.wsp:1]

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Drafts Pending Active

L1: (4) ("5578957" or ("5604449")).PN.
 L2: (662) High\$3 near10 (voltage\$1) same (reset\$3) and (L3: (2) ("5465054").PN.
 L4: (0) 2 and 348/308.1.ccis.
 L5: (525) 348/308.ccis.
 L6: (101) 5 and 2
 L7: (16477) oxide\$1 same breakdown\$3
 L8: (4) 7 and 2
 L9: (2) 7 and 348/308.ccis.
 L10: (45) 7 and reset\$3 same (voltage\$1) same (level\$31)
 L11: (120) 7 and reset\$3 same (voltage\$1) same (supply)
 L12: (1) 11 and 348/308.ccis.
 L13: (4) 11 and 250/5.ccis.
 L14: (25) 11 and 257/5.ccis.
 L15: (7) oxide\$1 same breakdown\$3 and (cmos or MOS) same
 L16: (9) 7 and 348/5.ccis. and reset\$3
 L17: (2) 2 and 348/5.ccis. and breakdown\$3

DB: USPA1;US-PPUB;EPO;JPO;DEPATENT;IBM;TOB
 Default operat: OR
 Purge
 Highlight hit terms only

BRD Form Back Form Image Text HTML

Type L Hits Search Text

1	IS&R	L 4	((("5578957" or ("5604449")).PN.	USP
2	BRS	L 662	High\$3 near10 (voltage\$1) same (reset\$3) and (MOS or CMOS) same (sensor\$1 or camera\$1)	USP
3	IS&R	L 2	("5465054").PN.	USP
4	BRS	L 0	2 and 348/308.1.ccis.	USP
5	BRS	L 525	348/308.ccis.	USP
6	BRS	L 101	5 and 2	USP
7	BRS	L 164	oxide\$1 same breakdown\$3	USP
8	BRS	L 4	7 and 2	USP
9	BRS	L 2	7 and 348/308.ccis.	USP
10	BRS	L 45	7 and reset\$3 same (voltage\$1) same (level\$31) same (supply)	USP
11	BRS	L 120	7 and reset\$3 same (voltage\$1) same (supply)	USP
12	BRS	L 1	11 and 348/308.ccis.	USP
13	BRS	L 4	11 and 250/5.ccis.	USP
14	BRS	L 25	11 and 257/5.ccis.	USP
15	BRS	L 7	oxide\$1 same breakdown\$3 and (cmos or MOS) same (sensor\$1 or camera\$1) and (reset\$3) same (voltage\$1) same (high\$3 or larg\$3 or above\$1 or g	USP
16	BRS	L 9	7 and 348/5.ccis. and reset\$3	USP
17	BRS	L 2	2 and 348/5.ccis. and breakdown\$3	USP
18	BRS	L 12	7 and reset\$3 same (sensor or APS or pixel\$1) same (CMOS or MOS)	USP
19	BRS	L 3	prevent\$3 same (breakdown\$1) same (CMOS or MOS or APS) same (sensor\$1 or pixel\$1 or camera\$1) and reset\$3	USP
20	BRS	L 63	prevent\$3 same (breakdown\$1 or oxide\$1) same (CMOS or MOS or APS) same (sensor\$1 or pixel\$1 or camera\$1) and reset\$3	USP
21	BRS	L 300	(gate\$1) same (oxide\$1) same (CMOS or MOS or APS) same (sensor\$1 or pixel\$1 or camera\$1) and reset\$3	USP
22	BRS	L 42	21 and 2	USP
23	BRS	L 231	250/208.1.ccis.	USP
24	BRS	L 540	257/291.ccis.	USP
25	BRS	L 602	257/292.ccis.	USP
26	BRS	L 98	348/307.ccis.	USP
27	BRS	L 298	348/302.ccis.	USP
28	BRS	L 23	7 and 348/5.ccis.	USP
29	BRS	L 30	6 and cathode\$1	USP
30	BRS	L 67	6 and high\$3 same (reset\$3) same (gate\$1)	USP

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